

**libswd**  
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# Chapter 1

## Serial Wire Debug Open Library.

### 1.1 Introduction

Welcome to the source code documentation repository. LibSWD is an Open-Source framework to deal with Serial Wire Debug. It is released under 3-clause BSD license. For more information please visit project website at <http://libswd.sf.net>

### 1.2 What is this about

Serial Wire Debug is an alternative to JTAG (IEEE1149.1) transport layer to access Debug Access Port in ARM-Cortex's based devices. LibSWD provides both bitstream generation and high/low level bus operations. Every bus operation such as request, turnaround, acknowledge, data and parity packet is represented by a `swd_cmd_t` element that can extend command queue (a standard bidirectional queue) that later can be flushed into real hardware using simple set of interface-specific driver functions. This way LibSWD is almost standalone and can be easily integrated into existing utilities for low-level access and only requires in return to define drivers that controls the interface interconnecting host and target. Such drivers are application specific therefore located in external file crafted for that application and its hardware.



# Chapter 2

## Class Index

### 2.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

<code>swd_ahbap_t</code> (Most actual Advanced High Bandwidth Access Peripherial Bus Reisters ) . . . . .	7
<code>swd_cmd_t</code> (SWD Command Element Structure ) . . . . .	8
<code>swd_context_config_t</code> (Context configuration structure ) . . . . .	9
<code>swd_ctx_t</code> (SWD Context Structure definition ) . . . . .	10
<code>swd_driver_t</code> (Interface Driver structure ) . . . . .	10
<code>swd_swdp_t</code> (Most actual Serial Wire Debug Port Registers ) . . . . .	11



# **Chapter 3**

## **File Index**

### **3.1 File List**

Here is a list of all documented files with brief descriptions:

<code>libswd.c</code>	.....	13
<code>libswd.h</code>	.....	31



# Chapter 4

## Class Documentation

### 4.1 swd\_ahbap\_t Struct Reference

Most actual Advanced High Bandwidth Access Peripherial Bus Reisters.

```
#include <libswd.h>
```

#### Public Attributes

- int **controlstatus**  
*Last known CONTROLSTATUS register value.*
- int **tar**  
*Last known TAR register value.*
- int **drw**  
*Last known DRW register value.*
- int **bd0**  
*Last known BD0 register value.*
- int **bd1**  
*Last known BD1 register value.*
- int **bd2**  
*Last known BD2 register value.*
- int **bd3**  
*Last known BD3 register value.*
- int **dromt**  
*Last known DROMT register value.*
- int **idr**  
*Last known IDR register value.*

### 4.1.1 Detailed Description

Most actual Advanced High Bandwidth Access Peripheral Bus Registers.

The documentation for this struct was generated from the following file:

- [libswd.h](#)

## 4.2 swd\_cmd\_t Struct Reference

SWD Command Element Structure.

```
#include <libswd.h>
```

### Public Attributes

- union {
   
    char **TRNnMOSI**  
       *< Payload data union.*  
     char **request**  
       *Request header data.*  
     char **ack**  
       *Acknowledge response from target.*  
     int **misodata**  
       *Data read from target (MISO).*  
     int **mosidata**  
       *Data written to target (MOSI).*  
     char **misobit**  
       *Single bit read from target (bit-per-char).*  
     char **mosibit**  
       *Single bit written to target (bit-per-char).*  
     char **parity**  
       *Parity bit for data payload.*  
     char **control**  
       *Control transfer data (one byte).*  
 };
- char **bits**  
*Payload bit count == clk pulses on the bus.*
- char **cmdtype**  
*Command type as defined by swd\_cmdtype\_t.*
- char **done**  
*Non-zero if operation already executed.*
- struct **swd\_cmd\_t \* prev**
- struct **swd\_cmd\_t \* next**  
*Pointer to the previous/next command.*

### 4.2.1 Detailed Description

SWD Command Element Structure. In libswd each operation is split into separate commands (request, trn, ack, data, parity) that can be appended to the command queue and later executed. This organization allows better granularity for tracing bugs and makes possible to compose complete bus/target operations made of simple commands.

### 4.2.2 Member Data Documentation

#### 4.2.2.1 char swd\_cmd\_t::TRNnMOSI

< Payload data union.

Holds/sets bus direction: MOSI when zero, MISO for other.

The documentation for this struct was generated from the following file:

- [libswd.h](#)

## 4.3 swd\_context\_config\_t Struct Reference

Context configuration structure.

```
#include <libswd.h>
```

### Public Attributes

- char [initialized](#)

*Context must be initialized prior use.*

- char [trnlen](#)

*How many CLK cycles will TRN use.*

- int [maxcmdqlen](#)

*How long command queue can be.*

- [swd\\_loglevel\\_t loglevel](#)

*Holds Logging Level setting.*

### 4.3.1 Detailed Description

Context configuration structure.

The documentation for this struct was generated from the following file:

- [libswd.h](#)

## 4.4 swd\_ctx\_t Struct Reference

SWD Context Structure definition.

```
#include <libswd.h>
```

### Public Attributes

- [swd\\_cmd\\_t \\* cmdq](#)  
*Command queue, stores all bus operations.*
- [swd\\_context\\_config\\_t config](#)  
*Target specific configuration.*
- [swd\\_driver\\_t \\* driver](#)  
*Pointer to the interface driver structure.*
- [swd\\_swdp\\_t misoswdp](#)  
*Last known read from the SW-DP register.*
- [swd\\_swdp\\_t mosiswdp](#)  
*Last known write to the SW-DP register.*
- [swd\\_ahbap\\_t misoahbap](#)  
*Last known read from AHB-AP register.*
- [swd\\_ahbap\\_t mosiahbap](#)  
*Last known write to the AHB-AP register.*

### 4.4.1 Detailed Description

SWD Context Structure definition. It stores all the information about the library, drivers and interface configuration, target status along with DAP/AHBAP data/instruction internal registers, and the command queue. Bus operations are stored on the command queue. There may be more than one context in use by a host software, each one for single interface-target pair. Most of the target operations made with libswd are required to pass [swd\\_ctx\\_t](#) pointer structure that also remembers last known state of the target's internal registers.

The documentation for this struct was generated from the following file:

- [libswd.h](#)

## 4.5 swd\_driver\_t Struct Reference

Interface Driver structure.

```
#include <libswd.h>
```

## Public Attributes

- void \* **device**

### 4.5.1 Detailed Description

Interface Driver structure. It holds pointer to the driver structure that keeps driver information necessary to work with the physical interface.

The documentation for this struct was generated from the following file:

- [libswd.h](#)

## 4.6 swd\_swdp\_t Struct Reference

Most actual Serial Wire Debug Port Registers.

```
#include <libswd.h>
```

## Public Attributes

- char **ack**  
*Last known state of ACK response.*
- int **idcode**  
*Target's IDCODE register value.*
- int **abort**  
*Last known ABORT register value.*
- int **ctrlstat**  
*Last known CTRLSTAT register value.*
- int **wcr**  
*Last known WCR register value.*
- int **select**  
*Last known SELECT register value.*
- int **rdbuf**  
*Last known RDBUF register (payload data) value.*

### 4.6.1 Detailed Description

Most actual Serial Wire Debug Port Registers.

The documentation for this struct was generated from the following file:

- [libswd.h](#)



# Chapter 5

## File Documentation

### 5.1 libswd.c File Reference

```
#include <libswd.h>
#include <urjtag/libswd.h>
#include <string.h>
#include <stdlib.h>
#include <stdio.h>
```

#### Functions

- int `swd_bin8_parity_even` (char \*data, char \*parity)  
*Data parity calculator; calculates even parity on char type.*
- int `swd_bin32_parity_even` (int \*data, char \*parity)  
*Data parity calculator; calculates even parity on integer type.*
- int `swd_bin8_print` (char \*data)  
*Prints binary data of a char value on the screen.*
- int `swd_bin32_print` (int \*data)  
*Prints binary data of an integer value on the screen.*
- char \* `swd_bin8_string` (char \*data)  
*Generates string containing binary data of a char value.*
- char \* `swd_bin32_string` (int \*data)  
*Generates string containing binary data of an integer value.*
- int `swd_bin8_bitswap` (unsigned char \*buffer, int bitcount)  
*Bit swap helper function that reverse bit order in char \*buffer.*
- int `swd_bin32_bitswap` (unsigned int \*buffer, int bitcount)

*Bit swap helper function that reverse bit order in int \*buffer.*

- int `swd_cmd_queue_init (swd_cmd_t *cmdq)`  
*Initialize new queue element in memory that becomes a queue root.*
- `swd_cmd_t * swd_cmd_queue_find_root (swd_cmd_t *cmdq)`  
*Find queue root (first element).*
- `swd_cmd_t * swd_cmd_queue_find_tail (swd_cmd_t *cmdq)`  
*Find queue tail (last element).*
- int `swd_cmd_queue_append (swd_cmd_t *cmdq, swd_cmd_t *cmd)`  
*Append element pointed by \*cmd at the end of the queqe pointed by \*cmdq.*
- int `swd_cmd_queue_free (swd_cmd_t *cmdq)`  
*Free queue pointed by \*cmdq element.*
- int `swd_cmd_queue_free_head (swd_cmd_t *cmdq)`  
*Free queue head up to \*cmdq element.*
- int `swd_cmd_queue_free_tail (swd_cmd_t *cmdq)`  
*Free queue tail starting after \*cmdq element.*
- int `swd_cmd_queue_append_mosi_request (swd_ctx_t *swdctx, char *request)`  
*Appends command queue with SWD Request packet header.*
- int `swd_cmd_queue_append_mosi_trn (swd_ctx_t *swdctx)`  
*Append command queue with Turnaround activating MOSI mode.*
- int `swd_cmd_queue_append_miso_trn (swd_ctx_t *swdctx)`  
*Append command queue with Turnaround activating MISO mode.*
- int `swd_cmd_queue_append_miso_nbit (swd_ctx_t *swdctx, char **data, int count)`  
*Append command queue with bus binary read bit-by-bit operation.*
- int `swd_cmd_queue_append_mosi_nbit (swd_ctx_t *swdctx, char *data, int count)`  
*Append command queue with bus binary write bit-by-bit operation.*
- int `swd_cmd_queue_append_mosi_parity (swd_ctx_t *swdctx, char *parity)`  
*Append command queue with parity bit write.*
- int `swd_cmd_queue_append_miso_parity (swd_ctx_t *swdctx, char *parity)`  
*Append command queue with parity bit read.*
- int `swd_cmd_queue_append_miso_data (swd_ctx_t *swdctx, int *data)`  
*Append command queue with data read.*
- int `swd_cmd_queue_append_miso_data_p (swd_ctx_t *swdctx, int *data, char *parity)`  
*Append command queue with data and parity read.*

- int `swd_cmd_queue_append_miso_n_data_p` (`swd_ctx_t` \*swdctx, int \*\*data, char \*\*parity, int count)  
*Append command queue with series of data and parity read.*
- int `swd_cmd_queue_append_mosi_data` (`swd_ctx_t` \*swdctx, int \*data)  
*Append command queue with data and parity write.*
- int `swd_cmd_queue_append_mosi_data_ap` (`swd_ctx_t` \*swdctx, int \*data)  
*Append command queue with data and automatic parity write.*
- int `swd_cmd_queue_append_mosi_data_p` (`swd_ctx_t` \*swdctx, int \*data, char \*parity)  
*Append command queue with data and provided parity write.*
- int `swd_cmd_append_mosi_n_data_ap` (`swd_ctx_t` \*swdctx, int \*\*data, int count)  
*Append command queue with series of data and automatic parity writes.*
- int `swd_cmd_append_mosi_n_data_p` (`swd_ctx_t` \*swdctx, int \*\*data, char \*\*parity, int count)  
*Append command queue with series of data and provided parity writes.*
- int `swd_cmd_queue_append_miso_ack` (`swd_ctx_t` \*swdctx, char \*ack)  
*Append queue with ACK read.*
- int `swd_cmd_queue_append_mosi_control` (`swd_ctx_t` \*swdctx, char \*ctlmsg, int len)  
*Append command queue with len-octet size control seruence.*
- int `swd_cmd_queue_append_swdpreset` (`swd_ctx_t` \*swdctx)  
*Append command queue with SW-DP-RESET sequence.*
- int `swd_cmd_queue_append_jtag2swd` (`swd_ctx_t` \*swdctx)  
*Append command queue with JTAG-TO-SWD DAP-switch sequence.*
- int `swd_cmd_queue_append_swd2jtag` (`swd_ctx_t` \*swdctx)  
*Append command queue with SWD-TO-JTAG DAP-switch sequence.*
- int `swd_bus_setdir_mosi` (`swd_ctx_t` \*swdctx)  
*Append command queue with TRN WRITE/MOSI.*
- int `swd_bus_setdir_miso` (`swd_ctx_t` \*swdctx)  
*Append command queue with TRN READ/MISO.*
- int `swd_bit8_gen_request` (`swd_ctx_t` \*swdctx, char \*APnDP, char \*RnW, char \*addr, char \*request)  
*Generate 8-bit SWD-REQUEST packet contents with provided parameters.*
- int `swd_transmit` (`swd_ctx_t` \*swdctx, `swd_cmd_t` \*cmd)  
*Transmit selected command to the interface driver.*
- int `swd_cmd_queue_flush` (`swd_ctx_t` \*swdctx, `swd_operation_t` operation)  
*Flush command queue contents into interface driver.*

- int `swd_mosi_request` (`swd_ctx_t` \*`swdctx`, `swd_operation_t` `operation`, `char` \*`APnDP`, `char` \*`RnW`, `char` \*`addr`)  
*Perform Request.*
- int `swd_miso_ack` (`swd_ctx_t` \*`swdctx`, `swd_operation_t` `operation`, `char` \*`ack`)  
*Perform ACK read into \*ack and verify received data.*
- int `swd_mosi_data_p` (`swd_ctx_t` \*`swdctx`, `swd_operation_t` `operation`, `int` \*`data`, `char` \*`parity`)  
*Perform (MOSI) data write with provided parity value.*
- int `swd_mosi_data_ap` (`swd_ctx_t` \*`swdctx`, `swd_operation_t` `operation`, `int` \*`data`)  
*Perform (MOSI) data write with automatic parity calculation.*
- int `swd_miso_data_p` (`swd_ctx_t` \*`swdctx`, `swd_operation_t` `operation`, `int` \*`data`, `char` \*`parity`)  
*Perform (MISO) data read.*
- int `swd_mosi_jtag2swd` (`swd_ctx_t` \*`swdctx`, `swd_operation_t` `operation`)  
*Switch DAP into SW-DP.*
- int `swd_jtag2swd` (`swd_ctx_t` \*`swdctx`, `swd_operation_t` `operation`)  
*Activate SW-DP and deactivate JTAG-DP by sending out JTAG-TO-SWD sequence.*
- int `swd_idcode` (`swd_ctx_t` \*`swdctx`, `swd_operation_t` `operation`, `int` \*`idcode`, `char` \*`ack`, `char` \*`parity`)  
*Read target's IDCODE register value.*
- int `swd_log` (`swd_loglevel_t` `loglevel`, `char` \*`msg`)
- `char` \* `swd_error_string` (`swd_error_code_t` `error`)
- `swd_ctx_t` \* `swd_init` (`void`)  
*LibSWD initialization routine.*
- int `swd_deinit_ctx` (`swd_ctx_t` \*`swdctx`)  
*De-initialize selected swd context and free its memory.*
- int `swd_deinit_cmdq` (`swd_ctx_t` \*`swdctx`)  
*De-initialize command queue and free its memory on selected swd context.*
- int `swd_deinit` (`swd_ctx_t` \*`swdctx`)  
*De-initialize selected swd context and its command queue.*

### 5.1.1 Detailed Description

### 5.1.2 Function Documentation

#### 5.1.2.1 int `swd_bin32_bitswap` ( `unsigned int` \* `buffer`, `int` `bitcount` )

Bit swap helper function that reverse bit order in `int` \*`buffer`.

Most Significant Bit becomes Least Significant Bit. It is possible to swap only n-bits from `int` (32-bit) \*`buffer`.

**Parameters**

*\*buffer* unsigned char (32-bit) data pointer.

*bitcount* how many bits to swap.

**Returns**

swapped bit count (positive) or error code (negative).

**5.1.2.2 int swd\_bin32\_parity\_even ( int \* *data*, char \* *parity* )**

Data parity calculator, calculates even parity on integer type.

**Parameters**

*\*data* source data pointer.

*\*parity* resulting data pointer.

**Returns**

negative value on error, 0 or 1 as parity result.

**5.1.2.3 int swd\_bin32\_print ( int \* *data* )**

Prints binary data of an integer value on the screen.

**Parameters**

*\*data* source data pointer.

**Returns**

number of characters printed.

**5.1.2.4 char\* swd\_bin32\_string ( int \* *data* )**

Generates string containing binary data of an integer value.

**Parameters**

*\*data* source data pointer.

**Returns**

pointer to the resulting string.

**5.1.2.5 int swd\_bin8\_bitswap ( unsigned char \* *buffer*, int *bitcount* )**

Bit swap helper function that reverse bit order in char \*buffer.

Most Significant Bit becomes Least Significant Bit. It is possible to swap only n-bits from char (8-bit) \*buffer.

**Parameters**

\**buffer* unsigned char (8-bit) data pointer.

*bitcount* how many bits to swap.

**Returns**

swapped bit count (positive) or error code (negative).

**5.1.2.6 int swd\_bin8\_parity\_even ( char \* *data*, char \* *parity* )**

Data parity calculator, calculates even parity on char type.

**Parameters**

\**data* source data pointer.

\**parity* resulting data pointer.

**Returns**

negative value on error, 0 or 1 as parity result.

**5.1.2.7 int swd\_bin8\_print ( char \* *data* )**

Prints binary data of a char value on the screen.

**Parameters**

\**data* source data pointer.

**Returns**

number of characters printed.

**5.1.2.8 char\* swd\_bin8\_string ( char \* *data* )**

Generates string containing binary data of a char value.

**Parameters**

\**data* source data pointer.

**Returns**

pointer to the resulting string.

**5.1.2.9 int swd\_bit8\_gen\_request ( *swd\_ctx\_t* \* *swdctx*, *char* \* *APnDP*, *char* \* *RnW*, *char* \* *addr*, *char* \* *request* )**

Generate 8-bit SWD-REQUEST packet contents with provided parameters.

Note that parity bit value is calculated automatically.

#### Parameters

\**swdctx* swd context pointer.  
\**APnDP* AccessPort (high) or DebugPort (low) access type pointer.  
\**RnW* Read (high) or Write (low) operation type pointer.  
\**addr* target register address value pointer.  
\**request* pointer where to store resulting packet.

#### Returns

number of generated packets (1), or SWD\_ERROR\_CODE on failure.

**5.1.2.10 int swd\_bus\_setdir\_miso ( *swd\_ctx\_t* \* *swdctx* )**

Append command queue with TRN READ/MISO.

#### Parameters

\**swdctx* swd context pointer.

#### Returns

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.1.2.11 int swd\_bus\_setdir\_mosi ( *swd\_ctx\_t* \* *swdctx* )**

Append command queue with TRN WRITE/MOSI.

#### Parameters

\**swdctx* swd context pointer.

#### Returns

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.1.2.12 int swd\_cmd\_append\_mosi\_n\_data\_ap ( *swd\_ctx\_t* \* *swdctx*, *int* \*\* *data*, *int* *count* )**

Append command queue with series of data and automatic parity writes.

#### Parameters

\**swdctx* swd context pointer.  
\*\**data* data value array pointer.

**count** number of (data+parity) elements to read.

#### Returns

number of elements appended (2\*count), or SWD\_ERROR\_CODE on failure.

**5.1.2.13 int swd\_cmd\_append\_mosi\_n\_data\_p ( swd\_ctx\_t \* swdctx, int \*\* data, char \*\* parity, int count )**

Append command queue with series of data and provided parity writes.

#### Parameters

\***swdctx** swd context pointer.

\*\***data** data value array pointer.

\*\***parity** parity value array pointer.

**count** number of (data+parity) elements to read.

#### Returns

number of elements appended (2\*count), or SWD\_ERROR\_CODE on failure.

**5.1.2.14 int swd\_cmd\_queue\_append ( swd\_cmd\_t \* cmdq, swd\_cmd\_t \* cmd )**

Append element pointed by \*cmd at the end of the quque pointed by \*cmdq.

#### Parameters

\***cmdq** pointer to any element on command queue

\***cmd** pointer to the command to be appended

#### Returns

number of appended elements (one), SWD\_ERROR\_CODE on failure

**5.1.2.15 int swd\_cmd\_queue\_append\_jtag2swd ( swd\_ctx\_t \* swdctx )**

Append command queue with JTAG-TO-SWD DAP-switch sequence.

#### Parameters

\***swdctx** swd context pointer.

#### Returns

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.1.2.16 int swd\_cmd\_queue\_append\_miso\_ack ( *swd\_ctx\_t* \* *swdctx*, *char* \* *ack* )**

Append queue with ACK read.

**Parameters**

\**swdctx* swd context pointer.  
\**ack* packet value pointer.

**Returns**

number of elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.1.2.17 int swd\_cmd\_queue\_append\_miso\_data ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data* )**

Append command queue with data read.

**Parameters**

\**swdctx* swd context pointer.  
\**data* data pointer.

**Returns**

of elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.1.2.18 int swd\_cmd\_queue\_append\_miso\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data*, *char* \* *parity* )**

Append command queue with data and parity read.

**Parameters**

\**swdctx* swd context pointer.  
\**data* data value pointer.  
\**parity* parity value pointer.

**Returns**

number of elements appended (2), or SWD\_ERROR\_CODE on failure.

**5.1.2.19 int swd\_cmd\_queue\_append\_miso\_n\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *int* \*\* *data*, *char* \*\* *parity*, *int* *count* )**

Append command queue with series of data and parity read.

**Parameters**

\**swdctx* swd context pointer.  
\*\**data* data value array pointer.

***\*\*parity*** parity value array pointer.  
***count*** number of (data+parity) elements to read.

### Returns

number of elements appended (2\*count), or SWD\_ERROR\_CODE on failure.

## 5.1.2.20 int swd\_cmd\_queue\_append\_miso\_nbit ( *swd\_ctx\_t \* swdctx, char \*\* data, int count* )

Append command queue with bus binary read bit-by-bit operation.

This function will append command to the queue for each bit, and store one bit into single char array element, so read is not constrained to 8 bits. On error memory is released and appropriate error code is returned. Important: Memory pointed by *\*data* must be allocated prior call!

### Parameters

***\*swdctx*** swd context pointer.  
***\*\*data*** allocated data array to write result into.  
***count*** number of bits to read (also the *\*\*data* size).

### Returns

number of elements processed, or SWD\_ERROR\_CODE on failure.

## 5.1.2.21 int swd\_cmd\_queue\_append\_miso\_parity ( *swd\_ctx\_t \* swdctx, char \* parity* )

Append command queue with parity bit read.

### Parameters

***\*swdctx*** swd context pointer.  
***\*parity*** parity value pointer.

### Returns

number of elements appended (1), or SWD\_ERROR\_CODE on failure.

## 5.1.2.22 int swd\_cmd\_queue\_append\_miso\_trn ( *swd\_ctx\_t \* swdctx* )

Append command queue with Turnaround activating MISO mode.

### Parameters

***\*swdctx*** swd context pointer.

### Returns

return number of elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.1.2.23 int swd\_cmd\_queue\_append\_mosi\_control ( *swd\_ctx\_t* \* *swdctx*, *char* \* *ctlmsg*, *int* *len* )**

Append command queue with len-octet size control seruence.

This control sequence can be used for instance to send payload of packets switching DAP between JTAG and SWD mode.

#### Parameters

\**swdctx* swd context pointer.  
\**ctlmsg* control message array pointer.  
*len* number of elements to send from \**ctlmsg*.

#### Returns

number of elements appended (*len*), or SWD\_ERROR\_CODE on failure.

**5.1.2.24 int swd\_cmd\_queue\_append\_mosi\_data ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data* )**

Append command queue with data and parity write.

#### Parameters

\**swdctx* swd context pointer.  
\**data* data value pointer.

#### Returns

number of elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.1.2.25 int swd\_cmd\_queue\_append\_mosi\_data\_ap ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data* )**

Append command queue with data and automatic parity write.

#### Parameters

\**swdctx* swd context pointer.  
\**data* data value pointer.

#### Returns

number of elements appended (2), or SWD\_ERROR\_CODE on failure.

**5.1.2.26 int swd\_cmd\_queue\_append\_mosi\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data*, *char* \* *parity* )**

Append command queue with data and provided parity write.

#### Parameters

\**swdctx* swd context pointer.

\****data*** data value pointer.  
\****parity*** parity value pointer.

#### Returns

number of elements appended (2), or SWD\_ERROR\_CODE on failure.

### 5.1.2.27 int swd\_cmd\_queue\_append\_mosi\_nbit ( ***swd\_ctx\_t*** \* ***swdctx***, ***char*** \* ***data***, ***int*** ***count*** )

Append command queue with bus binary write bit-by-bit operation.

This function will append command to the queue for each bit and store one bit into single char array element, so read is not constrained to 8 bits. On error memory is released and appropriate error code is returned. Important: Memory pointed by \*data must be allocated prior call!

#### Parameters

\****swdctx*** swd context pointer.  
\*\****data*** allocated data array to write result into.  
***count*** number of bits to read (also the \*\*data size).

#### Returns

number of elements processed, or SWD\_ERROR\_CODE on failure.

### 5.1.2.28 int swd\_cmd\_queue\_append\_mosi\_parity ( ***swd\_ctx\_t*** \* ***swdctx***, ***char*** \* ***parity*** )

Append command queue with parity bit write.

#### Parameters

\****swdctx*** swd context pointer.  
\****parity*** parity value pointer.

#### Returns

number of elements appended (1), or SWD\_ERROR\_CODE on failure.

### 5.1.2.29 int swd\_cmd\_queue\_append\_mosi\_request ( ***swd\_ctx\_t*** \* ***swdctx***, ***char*** \* ***request*** )

Appends command queue with SWD Request packet header.

Note that contents is not validated, so bad request can be sent as well.

#### Parameters

\****swdctx*** swd context pointer.  
\****request*** pointer to the 8-bit request payload.

#### Returns

return number elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.1.2.30 int swd\_cmd\_queue\_append\_mosi\_trn ( swd\_ctx\_t \* swdctx )**

Append command queue with Turnaround activating MOSI mode.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

return number elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.1.2.31 int swd\_cmd\_queue\_append\_swd2jtag ( swd\_ctx\_t \* swdctx )**

Append command queue with SWD-TO-JTAG DAP-switch sequence.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.1.2.32 int swd\_cmd\_queue\_append\_swdpreset ( swd\_ctx\_t \* swdctx )**

Append command queue with SW-DP-RESET sequence.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.1.2.33 swd\_cmd\_t\* swd\_cmd\_queue\_find\_root ( swd\_cmd\_t \* cmdq )**

Find queue root (first element).

**Parameters**

\**cmdq* pointer to any queue element

**Returns**

swd\_cmd\_t\* pointer to the first element (root), NULL on failure

**5.1.2.34 swd\_cmd\_t\* swd\_cmd\_queue\_find\_tail ( swd\_cmd\_t \* cmdq )**

Find queue tail (last element).

**Parameters**

*\*cmdq* pointer to any queue element

**Returns**

swd\_cmd\_t\* pointer to the last element (tail), NULL on failure

**5.1.2.35 int swd\_cmd\_queue\_flush ( swd\_ctx\_t \* swdctx, swd\_operation\_t operation )**

Flush command queue contents into interface driver.

Operation is specified by SWD\_OPERATION and can be used to select how to flush the queue, ie. head-only, tail-only, one, all, etc.

**Parameters**

*\*swdctx* swd context pointer.

*operation* tells how to flush the queue.

**Returns**

number of commands transmitted, or SWD\_ERROR\_CODE on failure.

**5.1.2.36 int swd\_cmd\_queue\_free ( swd\_cmd\_t \* cmdq )**

Free queue pointed by \*cmdq element.

**Parameters**

*\*cmdq* pointer to any element on command queue

**Returns**

number of elements destroyed, SWD\_ERROR\_CODE on failure

**5.1.2.37 int swd\_cmd\_queue\_free\_head ( swd\_cmd\_t \* cmdq )**

Free queue head up to \*cmdq element.

**Parameters**

*\*cmdq* pointer to the element that becomes new queue root.

**Returns**

number of elements destroyed, or SWD\_ERROR\_CODE on failure.

**5.1.2.38 int swd\_cmd\_queue\_free\_tail ( *swd\_cmd\_t* \* *cmdq* )**

Free queue tail starting after \*cmdq element.

**Parameters**

\**cmdq* pointer to the last element on the new queue.

**Returns**

number of elements destroyed, or SWD\_ERROR\_CODE on failure.

**5.1.2.39 int swd\_cmd\_queue\_init ( *swd\_cmd\_t* \* *cmdq* )**

Initialize new queue element in memory that becomes a queue root.

**Parameters**

\**cmdq* pointer to the command queue element of type [swd\\_cmd\\_t](#)

**Returns**

SWD\_OK on success, SWD\_ERROR\_CODE code on failure

**5.1.2.40 int swd\_deinit ( *swd\_ctx\_t* \* *swdctx* )**

De-initialize selected swd context and its command queue.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

number of elements freed, or SWD\_ERROR\_CODE on failure.

**5.1.2.41 int swd\_deinit\_cmdq ( *swd\_ctx\_t* \* *swdctx* )**

De-initialize command queue and free its memory on selected swd context.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

number of commands freed, or SWD\_ERROR\_CODE on failure.

### 5.1.2.42 int swd\_deinit\_ctx ( *swd\_ctx\_t* \* *swdctx* )

De-initialize selected swd context and free its memory.

Note: This function will not free command queue for selected context!

#### Parameters

*\*swdctx* swd context pointer.

#### Returns

SWD\_OK on success, SWD\_ERROR\_CODE on failure.

### 5.1.2.43 int swd\_idcode ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *int* \* *idcode*, *char* \* *ack*, *char* \* *parity* )

Read target's IDCODE register value.

#### Parameters

*\*swdctx* swd context pointer.

*operation* type of action to perform (queue or execute).

*\*idcode* resulting register value pointer.

*\*ack* resulting acknowledge response value pointer.

*\*parity* resulting data parity value pointer.

#### Returns

number of elements processed on the queue, or SWD\_ERROR\_CODE on failure.

### 5.1.2.44 *swd\_ctx\_t*\* swd\_init ( void )

LibSWD initialization routine.

It should be called prior any operation made with libswd. It initializes command queue and basic parameters for context that is returned as pointer.

#### Returns

pointer to the initialized swd context.

### 5.1.2.45 int swd\_jtag2swd ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation* )

Activate SW-DP and deactivate JTAG-DP by sending out JTAG-TO-SWD sequence.

#### Parameters

*\*swdctx* swd context.

#### Returns

number of control bytes executed, or error code on failre.

**5.1.2.46 int swd\_miso\_ack ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *char* \* *ack* )**

Perform ACK read into \*ack and verify received data.

**Parameters**

\**swdctx* swd context pointer.

*operation* type of action to perform with generated request.

\**ack* pointer to the result location.

**Returns**

number of commands processed, or SWD\_ERROR\_CODE on failure.

**5.1.2.47 int swd\_miso\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *int* \* *data*, *char* \* *parity* )**

Perform (MISO) data read.

**Parameters**

\**swdctx* swd context pointer.

*operation* type of action to perform on generated command.

\**data* payload value pointer.

\**parity* payload parity value pointer.

**Returns**

number of elements processed, or SWD\_ERROR\_CODE on failure.

**5.1.2.48 int swd\_mosi\_data\_ap ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *int* \* *data* )**

Perform (MOSI) data write with automatic parity calculation.

**Parameters**

\**swdctx* swd context pointer.

*operation* type of action to perform on generated command.

\**data* payload value pointer.

**Returns**

number of elements processed, or SWD\_ERROR\_CODE on failure.

**5.1.2.49 int swd\_mosi\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *int* \* *data*, *char* \* *parity* )**

Perform (MOSI) data write with provided parity value.

**Parameters**

*\*swdctx* swd context pointer.

*operation* type of action to perform on generated command.

*\*data* payload value pointer.

*\*parity* payload parity value pointer.

**Returns**

number of elements processed, or SWD\_ERROR\_CODE on failure.

**5.1.2.50 int swd\_mosi\_jtag2swd ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation* )**

Switch DAP into SW-DP.

According to ARM documentation target's DAP use JTAG transport by default and so JTAG-DP is active after power up. To use SWD user must perform predefined sequence on SWDIO/TMS lines, then read out the IDCODE to ensure proper SW-DP operation.

**5.1.2.51 int swd\_mosi\_request ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *char* \* *APnDP*, *char* \* *RnW*, *char* \* *addr* )**

Perform Request.

**Parameters**

*\*swdctx* swd context pointer.

*operation* type of action to perform with generated request.

*\*APnDP* AccessPort (high) or DebugPort (low) access value pointer.

*\*RnW* Read (high) or Write (low) access value pointer.

*\*addr* target register address value pointer.

**Returns**

number of commands processed, or SWD\_ERROR\_CODE on failure.

**5.1.2.52 int swd\_transmit ( *swd\_ctx\_t* \* *swdctx*, *swd\_cmd\_t* \* *cmd* )**

Transmit selected command to the interface driver.

**Parameters**

*\*swdctx* swd context pointer.

*\*cmd* pointer to the command to be sent.

**Returns**

number of commands transmitted (1), or SWD\_ERROR\_CODE on failure.

## 5.2 libswd.h File Reference

### Classes

- struct [swd\\_cmd\\_t](#)  
*SWD Command Element Structure.*
- struct [swd\\_context\\_config\\_t](#)  
*Context configuration structure.*
- struct [swd\\_swdp\\_t](#)  
*Most actual Serial Wire Debug Port Registers.*
- struct [swd\\_ahbap\\_t](#)  
*Most actual Advanced High Bandwidth Access Peripheral Bus Registers.*
- struct [swd\\_driver\\_t](#)  
*Interface Driver structure.*
- struct [swd\\_ctx\\_t](#)  
*SWD Context Structure definition.*

### Defines

- #define [SWD\\_REQUEST\\_START\\_BITNUM](#) 7  
*SWD Packets Bit Fields and Values.*
- #define [SWD\\_REQUEST\\_APnDP\\_BITNUM](#) 6  
*Access Port (high) or Debug Port (low) access.*
- #define [SWD\\_REQUEST\\_RnW\\_BITNUM](#) 5  
*Read (high) or Write (low) access.*
- #define [SWD\\_REQUEST\\_ADDR\\_BITNUM](#) 4  
*LSB of the address field in request header.*
- #define [SWD\\_REQUEST\\_A2\\_BITNUM](#) 4  
*Target Register Address bit 2.*
- #define [SWD\\_REQUEST\\_A3\\_BITNUM](#) 3  
*Target Register Address bit 3.*
- #define [SWD\\_REQUEST\\_PARITY\\_BITNUM](#) 2  
*Odd Parity calculated from APnDP, RnW, A[2:3].*
- #define [SWD\\_REQUEST\\_STOP\\_BITNUM](#) 1  
*Packet Stop bit, always 0.*

- #define **SWD\_REQUEST\_PARK\_BITNUM** 0  
*Park wire and switch between receive/transmit.*
- #define **SWD\_REQUEST\_START\_VAL** 1  
*Start Bit Value is always 1.*
- #define **SWD\_REQUEST\_STOP\_VAL** 0  
*Stop Bit Value is always 0.*
- #define **SWD\_REQUEST\_PARK\_VAL** 1  
*Park bus and put outputs into Hi-Z state.*
- #define **SWD\_REQUEST\_BITLEN** 8  
*Number of bits in request packet header.*
- #define **SWD\_ADDR\_MINVAL** 0  
*Address field minimal value.*
- #define **SWD\_ADDR\_MAXVAL** 3  
*Address field maximal value.*
- #define **SWD\_ACK\_BITLEN** 3  
*Number of bits in Acknowledge packet.*
- #define **SWD\_ACK\_OK\_VAL** 4  
*OK code value.*
- #define **SWD\_ACK\_WAIT\_VAL** 2  
*WAIT code value.*
- #define **SWD\_ACK\_FAULT\_VAL** 1  
*FAULT code value.*
- #define **SWD\_DP\_ADDR\_IDCODE** 0  
*IDCODE register address (RO).*
- #define **SWD\_DP\_ADDR\_ABORT** 0  
*ABORT register address (WO).*
- #define **SWD\_DP\_ADDR\_CTRLSTAT** 1  
*CTRLSTAT register address (R/W, CTRLSEL=b0).*
- #define **SWD\_DP\_ADDR\_WCR** 1  
*WCR register address (R/W, CTRLSEL=b1).*
- #define **SWD\_DP\_ADDR\_RESEND** 2  
*RESEND register address (RO).*
- #define **SWD\_DP\_ADDR\_SELECT** 2  
*SELECT register address (WO).*

- #define **SWD\_DP\_ADDR\_RDBUF** 3  
*RDBUF register address (RO).*
- #define **SWD\_ABORT\_BITNUM\_DAPABORT** 0  
*SW-DP ABORT Register map.*
- #define **SWD\_ABORT\_BITNUM\_DSTKCMPCCLR** 1  
*DSTKCMPCCLR bit number.*
- #define **SWD\_ABORT\_BITNUM\_DSTKERRCLR** 2  
*DSTKERRCLR bit number.*
- #define **SWD\_ABORT\_BITNUM\_DWDERRCLR** 3  
*DWDERRCLR bit number.*
- #define **SWD\_ABORT\_BITNUM\_DORUNERRCLR** 4  
*DORUNERRCLR bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_ORUNDETECT** 0  
*SW-DP CTRL/STAT Register map.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OSTICKYORUN** 1  
*OSTICKYORUN bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OTRNMODE** 2  
*OTRNMODE bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OSTICKYCMP** 4  
*OSTICKYCMP bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OSTICKYERR** 5  
*OSTICKYERR bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OREADOK** 6  
*OREADOK bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OWDATAERR** 7  
*OWDATAERR bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OMASKLANE** 8  
*OMASKLANE bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OTRNCNT** 12  
*OTRNCNT bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OCDBGREQ** 26  
*OCDBGREQ bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OCDBGSTACK** 27

*OCDBGRTACK bit number.*

- #define **SWD\_CTRLSTAT\_BITNUM\_OCDBGWRUPREQ** 28  
*OCDBGWRUPREQ bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OCDBGWRUPACK** 29  
*OCDBGWRUPACK bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OCSYSPWRUPREQ** 30  
*OCSYSPWRUPREQ bit number.*
- #define **SWD\_CTRLSTAT\_BITNUM\_OCSYSPWRUPACK** 31  
*OCSYSPWRUPACK bit number.*
- #define **SWD\_MASKLANE\_0** 0b0001  
*SW-DP CTRLSTAT MASKLANE available values.*
- #define **SWD\_MASKLANE\_1** 0b0010  
*Compare byte lane 1 (0x----FF--).*
- #define **SWD\_MASKLANE\_2** 0b0100  
*Compare byte lane 2 (0x--FF----).*
- #define **SWD\_MASKLANE\_3** 0b1000  
*Compare byte lane 3 (0xFF-----).*
- #define **SWD\_SELECT\_BITNUM\_CTRLSEL** 0  
*SW-DP SELECT Register map.*
- #define **SWD\_SELECT\_BITNUM\_APBANKSEL** 4  
*APBANKSEL bit number.*
- #define **SWD\_SELECT\_BITNUM\_APSEL** 24  
*APSEL bit number.*
- #define **SWD\_WCR\_BITNUM\_PRESCALER** 0  
*SW-DP WCR Register map.*
- #define **SWD\_WCR\_BITNUM\_WIREMODE** 6
- #define **SWD\_WCR\_BITNUM\_TURNROUND** 8
- #define **SWD\_TURNROUND\_1** 0  
*SW-DP WCR TURNROUND available values.*
- #define **SWD\_TURNROUND\_2** 1
- #define **SWD\_TURNROUND\_3** 2
- #define **SWD\_TURNROUND\_4** 3
- #define **SWD\_TURNROUND\_MIN** SWD\_TURNROUND\_1
- #define **SWD\_TURNROUND\_MAX** SWD\_TURNROUND\_4
- #define **SWD\_TURNROUND\_DEFAULT** SWD\_TURNROUND\_1
- #define **AHB\_AP\_CONTROLSTATUS** 0x00

*AHB-AP Registers Map.*

- #define **AHB\_AP\_TAR** 0x04  
*R/W, 32bit, reset value: 0x00000000.*
- #define **AHB\_AP\_DRW** 0x0C  
*R/W, 32bit.*
- #define **AHB\_AP\_BD0** 0x10  
*R/W, 32bit.*
- #define **AHB\_AP\_BD1** 0x14  
*R/W, 32bit.*
- #define **AHB\_AP\_BD2** 0x18  
*R/W, 32bit.*
- #define **AHB\_AP\_BD3** 0x1C  
*R/W, 32bit.*
- #define **AHB\_AP\_DROMT** 0xF8  
*RO, 32bit, reset value: 0xE00FF000.*
- #define **AHB\_AP\_IDR** 0xFC  
*RO, 32bit, reset value: 0x24770001.*
- #define **SWD\_DATA\_MAXBITCOUNT** 32  
*SWD queue and payload data definitions.*
- #define **SWD\_DATA\_BYTESIZE** 8  
*How many bits are there in a byte.*
- #define **SWD\_DATA\_BITLEN** 32  
*How many bits are there in data payload.*
- #define **SWD\_CMDQLEN\_DEFAULT** 1024;  
*How long is the command queue by default.*

## Typedefs

- typedef enum **SWD\_ERROR\_CODE** **swd\_error\_code\_t**  
*Status and Error Codes definitions.*
- typedef enum **SWD\_LOGLEVEL** **swd\_loglevel\_t**  
*Logging Level Codes definition.*
- typedef enum **SWD\_CMDTYPE** **swd\_cmdtype\_t**  
*SWD Command Codes definitions.*

- **typedef enum SWD\_SHIFTDIR swd\_shiftdir\_t**  
*What is the shift direction LSB-first or MSB-first.*
- **typedef enum SWD\_OPERATION swd\_operation\_t**  
*Command Queue operations codes.*
- **typedef struct swd\_cmd\_t swd\_cmd\_t**  
*SWD Command Element Structure.*

## Enumerations

- **enum SWD\_ERROR\_CODE {**

**SWD\_OK** = 0, **SWD\_ERROR\_GENERAL** = -1, **SWD\_ERROR\_NULLPOINTER** = -2, **SWD\_ERROR\_NULLQUEUE** = -3,  
**SWD\_ERROR\_NULLTRN** = -4, **SWD\_ERROR\_PARAM** = -5, **SWD\_ERROR\_OUTOFMEM** = -6,  
**SWD\_ERROR\_RESULT** = -7,  
**SWD\_ERROR\_RANGE** = -8, **SWD\_ERROR\_DEFINITION** = -9, **SWD\_ERROR\_NULLCONTEXT** = -10, **SWD\_ERROR\_QUEUE** = -11,  
**SWD\_ERROR\_ADDR** = -12, **SWD\_ERROR\_APnDP** = -13, **SWD\_ERROR\_RnW** = -14, **SWD\_ERROR\_PARITY** = -15,  
**SWD\_ERROR\_ACK** = -16, **SWD\_ERROR\_ACKUNKNOWN** = -19, **SWD\_ERROR\_ACKNOTDONE** = -20, **SWD\_ERROR\_ACKMISSING** = -21,  
**SWD\_ERROR\_ACKMISMATCH** = -22, **SWD\_ERROR\_ACKORDER** = -23, **SWD\_ERROR\_BADOPCODE** = -24, **SWD\_ERROR\_NODATACMD** = -25,  
**SWD\_ERROR\_DATAADDR** = -26, **SWD\_ERROR\_NOPARITYCMD** = -27, **SWD\_ERROR\_PARITYADDR** = -28, **SWD\_ERROR\_NOTDONE** = -29,  
**SWD\_ERROR\_QUEUEROOT** = -30, **SWD\_ERROR\_BADCMDTYPE** = -31, **SWD\_ERROR\_BADCMDDATA** = -32, **SWD\_ERROR\_TURNAROUND** = -33,  
**SWD\_ERROR\_DRIVER** = -34, **SWD\_ERROR\_ACK\_WAIT** = -35, **SWD\_ERROR\_ACK\_FAULT** = -36, **SWD\_ERROR\_QUEUENOTFREE** = -37,  
**SWD\_ERROR\_TRANSPORT** = -38 }

*Status and Error Codes definitions.*
- **enum SWD\_LOGLEVEL {**

**SWD\_LOGLEVEL\_SILENT** = 0, **SWD\_LOGLEVEL\_INFO** = 1, **SWD\_LOGLEVEL\_WARNING** = 2, **SWD\_LOGLEVEL\_ERROR** = 3,  
**SWD\_LOGLEVEL\_DEBUG** = 4 }

*Logging Level Codes definition.*
- **enum SWD\_CMDTYPE {**

**SWD\_CMDTYPE\_MOSI\_DATA** = -7, **SWD\_CMDTYPE\_MOSI\_REQUEST** = -6, **SWD\_CMDTYPE\_MOSI\_TRN** = -5, **SWD\_CMDTYPE\_MOSI\_PARITY** = -4,  
**SWD\_CMDTYPE\_MOSI\_BITBANG** = -3, **SWD\_CMDTYPE\_MOSI\_CONTROL** = -2, **SWD\_CMDTYPE\_MOSI** = -1, **SWD\_CMDTYPE\_UNDEFINED** = 0,  
**SWD\_CMDTYPE\_MISO** = 1, **SWD\_CMDTYPE\_MISO\_ACK** = 2, **SWD\_CMDTYPE\_MISO\_BITBANG** = 3, **SWD\_CMDTYPE\_MISO\_PARITY** = 4,  
**SWD\_CMDTYPE\_MISO\_TRN** = 5, **SWD\_CMDTYPE\_MISO\_DATA** = 6 }

*SWD Command Codes definitions.*

- enum `SWD_SHIFTDIR` { `SWD_DIR_LSBFIRST` = 0, `SWD_DIR_MSBFIRST` = 1 }

*What is the shift direction LSB-first or MSB-first.*

- enum `SWD_OPERATION` {  
`SWD_OPERATION_FIRST` = 1, `SWD_OPERATION_QUEUE_APPEND` = 1, `SWD_OPERATION_TRANSMIT_HEAD` = 2, `SWD_OPERATION_TRANSMIT_TAIL` = 3,  
`SWD_OPERATION_TRANSMIT_ALL` = 4, `SWD_OPERATION_TRANSMIT_ONE` = 5, `SWD_OPERATION_TRANSMIT_LAST` = 6, `SWD_OPERATION_EXECUTE` = 7,  
`SWD_OPERATION_LAST` = 7 }

*Command Queue operations codes.*

- enum `swd_bool_t` { `SWD_FALSE` = 0, `SWD_TRUE` = 1 }

*Boolean values definition.*

## Functions

- int `swd_bin8_parity_even` (char \*data, char \*parity)  
*Data parity calculator; calculates even parity on char type.*
- int `swd_bin32_parity_even` (int \*data, char \*parity)  
*Data parity calculator; calculates even parity on integer type.*
- int `swd_bin8_print` (char \*data)  
*Prints binary data of a char value on the screen.*
- int `swd_bin32_print` (int \*data)  
*Prints binary data of an integer value on the screen.*
- char \* `swd_bin8_string` (char \*data)  
*Generates string containing binary data of a char value.*
- char \* `swd_bin32_string` (int \*data)  
*Generates string containing binary data of an integer value.*
- int `swd_bin8_bitswap` (unsigned char \*buffer, int bitcount)  
*Bit swap helper function that reverse bit order in char \*buffer.*
- int `swd_bin32_bitswap` (unsigned int \*buffer, int bitcount)  
*Bit swap helper function that reverse bit order in int \*buffer.*
- int `swd_cmd_queue_init` (`swd_cmd_t` \*cmdq)  
*Initialize new queue element in memory that becomes a queue root.*
- `swd_cmd_t` \* `swd_cmd_queue_find_root` (`swd_cmd_t` \*cmdq)  
*Find queue root (first element).*

- `swd_cmd_t * swd_cmd_queue_find_tail (swd_cmd_t *cmdq)`  
*Find queue tail (last element).*
- `int swd_cmd_queue_append (swd_cmd_t *cmdq, swd_cmd_t *cmd)`  
*Append element pointed by \*cmd at the end of the queue pointed by \*cmdq.*
- `int swd_cmd_queue_free (swd_cmd_t *cmdq)`  
*Free queue pointed by \*cmdq element.*
- `int swd_cmd_queue_free_head (swd_cmd_t *cmdq)`  
*Free queue head up to \*cmdq element.*
- `int swd_cmd_queue_free_tail (swd_cmd_t *cmdq)`  
*Free queue tail starting after \*cmdq element.*
- `int swd_cmd_queue_append_mosi_request (swd_ctx_t *swdctx, char *request)`  
*Appends command queue with SWD Request packet header.*
- `int swd_cmd_queue_append_mosi_trn (swd_ctx_t *swdctx)`  
*Append command queue with Turnaround activating MOSI mode.*
- `int swd_cmd_queue_append_miso_trn (swd_ctx_t *swdctx)`  
*Append command queue with Turnaround activating MISO mode.*
- `int swd_cmd_queue_append_miso_nbit (swd_ctx_t *swdctx, char **data, int count)`  
*Append command queue with bus binary read bit-by-bit operation.*
- `int swd_cmd_queue_append_mosi_nbit (swd_ctx_t *swdctx, char *data, int count)`  
*Append command queue with bus binary write bit-by-bit operation.*
- `int swd_cmd_queue_append_mosi_parity (swd_ctx_t *swdctx, char *parity)`  
*Append command queue with parity bit write.*
- `int swd_cmd_queue_append_miso_parity (swd_ctx_t *swdctx, char *parity)`  
*Append command queue with parity bit read.*
- `int swd_cmd_queue_append_miso_data (swd_ctx_t *swdctx, int *data)`  
*Append command queue with data read.*
- `int swd_cmd_queue_append_miso_data_p (swd_ctx_t *swdctx, int *data, char *parity)`  
*Append command queue with data and parity read.*
- `int swd_cmd_queue_append_miso_n_data_p (swd_ctx_t *swdctx, int **data, char **parity, int count)`  
*Append command queue with series of data and parity read.*
- `int swd_cmd_queue_append_mosi_data (swd_ctx_t *swdctx, int *data)`  
*Append command queue with data and parity write.*
- `int swd_cmd_queue_append_mosi_data_ap (swd_ctx_t *swdctx, int *data)`

*Append command queue with data and automatic parity write.*

- int `swd_cmd_queue_append_mosi_data_p (swd_ctx_t *swdctx, int *data, char *parity)`  
*Append command queue with data and provided parity write.*
- int `swd_cmd_append_mosi_n_data_ap (swd_ctx_t *swdctx, int **data, int count)`  
*Append command queue with series of data and automatic parity writes.*
- int `swd_cmd_append_mosi_n_data_p (swd_ctx_t *swdctx, int **data, char **parity, int count)`  
*Append command queue with series of data and provided parity writes.*
- int `swd_cmd_queue_append_miso_ack (swd_ctx_t *swdctx, char *ack)`  
*Append queue with ACK read.*
- int `swd_cmd_queue_append_mosi_control (swd_ctx_t *swdctx, char *ctlmsg, int len)`  
*Append command queue with len-octet size control seruence.*
- int `swd_cmd_queue_append_swdpreset (swd_ctx_t *swdctx)`  
*Append command queue with SW-DP-RESET sequence.*
- int `swd_cmd_queue_append_jtag2swd (swd_ctx_t *swdctx)`  
*Append command queue with JTAG-TO-SWD DAP-switch sequence.*
- int `swd_cmd_queue_append_swd2jtag (swd_ctx_t *swdctx)`  
*Append command queue with SWD-TO-JTAG DAP-switch sequence.*
- int `swd_bus_setdir_mosi (swd_ctx_t *swdctx)`  
*Append command queue with TRN WRITE/MOSI.*
- int `swd_bus_setdir_miso (swd_ctx_t *swdctx)`  
*Append command queue with TRN READ/MISO.*
- int `swd_bit8_gen_request (swd_ctx_t *swdctx, char *APnDP, char *RnW, char *addr, char *request)`  
*Generate 8-bit SWD-REQUEST packet contents with provided parameters.*
- int `swd_transmit (swd_ctx_t *swdctx, swd_cmd_t *cmd)`  
*Transmit selected command to the interface driver.*
- int `swd_cmd_queue_flush (swd_ctx_t *swdctx, swd_operation_t operation)`  
*Flush command queue contents into interface driver.*
- int `swd_mosi_request (swd_ctx_t *swdctx, swd_operation_t operation, char *APnDP, char *RnW, char *addr)`  
*Perform Request.*
- int `swd_miso_ack (swd_ctx_t *swdctx, swd_operation_t operation, char *ack)`  
*Perform ACK read into \*ack and verify received data.*
- int `swd_mosi_data_p (swd_ctx_t *swdctx, swd_operation_t operation, int *data, char *parity)`

*Perform (MOSI) data write with provided parity value.*

- int **swd\_mosi\_data\_ap** (**swd\_ctx\_t** \*swdctx, **swd\_operation\_t** operation, int \*data)

*Perform (MOSI) data write with automatic parity calculation.*

- int **swd\_miso\_data\_p** (**swd\_ctx\_t** \*swdctx, **swd\_operation\_t** operation, int \*data, char \*parity)

*Perform (MISO) data read.*

- int **swd\_mosi\_jtag2swd** (**swd\_ctx\_t** \*swdctx, **swd\_operation\_t** operation)

*Switch DAP into SW-DP.*

- int **swd\_jtag2swd** (**swd\_ctx\_t** \*swdctx, **swd\_operation\_t** operation)

*Activate SW-DP and deactivate JTAG-DP by sending out JTAG-TO-SWD sequence.*

- int **swd\_idcode** (**swd\_ctx\_t** \*swdctx, **swd\_operation\_t** operation, int \*idcode, char \*ack, char \*parity)

*Read target's IDCODE register value.*

- int **swd\_log** (**swd\_loglevel\_t** loglevel, char \*msg)

- char \* **swd\_error\_string** (**swd\_error\_code\_t** error)

- **swd\_ctx\_t** \* **swd\_init** (void)

*LibSWD initialization routine.*

- int **swd\_deinit\_ctx** (**swd\_ctx\_t** \*swdctx)

*De-initialize selected swd context and free its memory.*

- int **swd\_deinit\_cmdq** (**swd\_ctx\_t** \*swdctx)

*De-initialize command queue and free its memory on selected swd context.*

- int **swd\_deinit** (**swd\_ctx\_t** \*swdctx)

*De-initialize selected swd context and its command queue.*

- int **swd\_drv\_mosi\_8** (**swd\_ctx\_t** \*swdctx, char \*data, int bits, int direction)

- int **swd\_drv\_mosi\_32** (**swd\_ctx\_t** \*swdctx, int \*data, int bits, int direction)

- int **swd\_drv\_miso\_8** (**swd\_ctx\_t** \*swdctx, char \*data, int bits, int direction)

- int **swd\_drv\_miso\_32** (**swd\_ctx\_t** \*swdctx, int \*data, int bits, int direction)

- int **swd\_drv\_mosi\_trn** (**swd\_ctx\_t** \*swdctx, int clks)

- int **swd\_drv\_miso\_trn** (**swd\_ctx\_t** \*swdctx, int clks)

## 5.2.1 Detailed Description

### 5.2.2 Define Documentation

#### 5.2.2.1 #define AHB\_AP\_BD0 0x10

R/W, 32bit.

R/W, 32bit

**5.2.2.2 #define AHB\_AP\_BD1 0x14**

R/W, 32bit.

R/W, 32bit

**5.2.2.3 #define AHB\_AP\_BD2 0x18**

R/W, 32bit.

R/W, 32bit

**5.2.2.4 #define AHB\_AP\_BD3 0x1C**

R/W, 32bit.

R/W, 32bit

**5.2.2.5 #define AHB\_AP\_CONTROLSTATUS 0x00**

AHB-AP Registers Map.

TODO!!!! R/W, 32bit, reset value: 0x43800042 R/W, 32bit, reset value: 0x43800042

**5.2.2.6 #define AHB\_AP\_DROMT 0xF8**

RO, 32bit, reset value: 0xE00FF000.

RO, 32bit, reset value: 0xE00FF000

**5.2.2.7 #define AHB\_AP\_DRW 0x0C**

R/W, 32bit.

R/W, 32bit

**5.2.2.8 #define AHB\_AP\_IDR 0xFC**

RO, 32bit, reset value: 0x24770001.

RO, 32bit, reset value: 0x24770001

**5.2.2.9 #define AHB\_AP\_TAR 0x04**

R/W, 32bit, reset value: 0x00000000.

R/W, 32bit, reset value: 0x00000000

**5.2.2.10 #define SWD\_ABORT\_BITNUM\_DAPABORT 0**

SW-DP ABORT Register map.

DAPABORT bit number.

**5.2.2.11 #define SWD\_CTRLSTAT\_BITNUM\_ORUNDETECT 0**

SW-DP CTRL/STAT Register map.

ORUNDETECT bit number.

**5.2.2.12 #define SWD\_DATA\_MAXBITCOUNT 32**

SWD queue and payload data definitions.

What is the maximal bit length of the data.

**5.2.2.13 #define SWD\_MASKLANE\_0 0b0001**

SW-DP CTRLSTAT MASKLANE available values.

Compare byte lane 0 (0x-----FF)

**5.2.2.14 #define SWD\_REQUEST\_START\_BITNUM 7**

SWD Packets Bit Fields and Values.

Packet Start bit, always set to 1.

**5.2.2.15 #define SWD\_SELECT\_BITNUM\_CTRLSEL 0**

SW-DP SELECT Register map.

CTRLSEL bit number.

**5.2.2.16 #define SWD\_TURNROUND\_1 0**

SW-DP WCR TURNROUND available values.

TRN takes one CLK cycle. TRN takes one CLK cycle.

**5.2.2.17 #define SWD\_TURNROUND\_2 1**

TRN takes two CLK cycles.

**5.2.2.18 #define SWD\_TURNROUND\_3 2**

TRN takes three CLK cycles.

**5.2.2.19 #define SWD\_TURNROUND\_4 3**

TRN takes four CLK cycles. ?????

**5.2.2.20 #define SWD\_TURNROUND\_DEFAULT SWD\_TURNROUND\_1**

Default TRN length is one CLK.

**5.2.2.21 #define SWD\_TURNROUND\_MAX SWD\_TURNROUND\_4**

longest TRN time.

**5.2.2.22 #define SWD\_TURNROUND\_MIN SWD\_TURNROUND\_1**

shortest TRN time.

**5.2.2.23 #define SWD\_WCR\_BITNUM\_PRESCALER 0**

SW-DP WCR Register map.

PRESCALER bit number. PRESCALER bit number.

**5.2.2.24 #define SWD\_WCR\_BITNUM\_TURNROUND 8**

TURNROUND bit number.

**5.2.2.25 #define SWD\_WCR\_BITNUM\_WIREMODE 6**

WIREMODE bit number.

## 5.2.3 Typedef Documentation

**5.2.3.1 typedef struct swd\_cmd\_t swd\_cmd\_t**

SWD Command Element Structure.

In libswd each operation is split into separate commands (request, trn, ack, data, parity) that can be appended to the command queue and later executed. This organization allows better granularity for tracing bugs and makes possible to compose complete bus/target operations made of simple commands.

**5.2.3.2 typedef enum SWD\_CMDTYPE swd\_cmdtype\_t**

SWD Command Codes definitions.

Available values: MISO>0, MOSI<0, undefined=0. To check command direction (read/write) multiply tested value with one of the MOSI or MISO commands

- result is positive for equal direction and negative if direction differs. Command Type codes definition, use this to see names in debugger.

**5.2.3.3 typedef enum SWD\_ERROR\_CODE swd\_error\_code\_t**

Status and Error Codes definitions.

Error Codes definition, use this to have its name on debugger.

### 5.2.3.4 `typedef enum SWD_LOGLEVEL swd_loglevel_t`

Logging Level Codes definition.

Logging Level codes definition, use this to have its name on debugger.

### 5.2.3.5 `typedef enum SWD_OPERATION swd_operation_t`

Command Queue operations codes.

### 5.2.3.6 `typedef enum SWD_SHIFTDIR swd_shiftdir_t`

What is the shift direction LSB-first or MSB-first.

## 5.2.4 Enumeration Type Documentation

### 5.2.4.1 `enum swd_bool_t`

Boolean values definition.

#### Enumerator:

`SWD_FALSE` False is 0.

`SWD_TRUE` True is 1.

### 5.2.4.2 `enum SWD_CMDTYPE`

SWD Command Codes definitions.

Available values: MISO>0, MOSI<0, undefined=0. To check command direction (read/write) multiply tested value with one of the MOSI or MISO commands

- result is positive for equal direction and negative if direction differs. Command Type codes definition, use this to see names in debugger.

#### Enumerator:

`SWD_CMDTYPE_MOSI_DATA` Contains MOSI data (from host).

`SWD_CMDTYPE_MOSI_REQUEST` Contains MOSI request packet.

`SWD_CMDTYPE_MOSI_TRN` Bus will switch into MOSI mode.

`SWD_CMDTYPE_MOSI_PARITY` Contains MOSI data parity.

`SWD_CMDTYPE_MOSI_BITBANG` Allows MOSI operation bit-by-bit.

`SWD_CMDTYPE_MOSI_CONTROL` MOSI control sequence (ie. sw-dp reset).

`SWD_CMDTYPE_MOSI` Master Output Slave Input direction.

`SWD_CMDTYPE_UNDEFINED` undefined command, not transmitted.

`SWD_CMDTYPE_MISO` Master Input Slave Output direction.

`SWD_CMDTYPE_MISO_ACK` Contains ACK data from target.

`SWD_CMDTYPE_MISO_BITBANG` Allows MISO operation bit-by-bit.

***SWD\_CMDTYPE\_MISO\_PARITY*** Contains MISO data parity.  
***SWD\_CMDTYPE\_MISO\_TRN*** Bus will switch into MISO mode.  
***SWD\_CMDTYPE\_MISO\_DATA*** Contains MISO data (from target).

#### 5.2.4.3 enum SWD\_ERROR\_CODE

Status and Error Codes definitions.

Error Codes definition, use this to have its name on debugger.

**Enumerator:**

***SWD\_OK*** No error.  
***SWD\_ERROR\_GENERAL*** General error.  
***SWD\_ERROR\_NULLPOINTER*** Null pointer.  
***SWD\_ERROR\_NULLQUEUE*** Null queue pointer.  
***SWD\_ERROR\_NULLTRN*** Null TurnaRouNd pointer.  
***SWD\_ERROR\_PARAM*** Bad parameter.  
***SWD\_ERROR\_OUTOFMEM*** Out of memory.  
***SWD\_ERROR\_RESULT*** Bad result.  
***SWD\_ERROR\_RANGE*** Out of range.  
***SWD\_ERROR\_DEFINITION*** Definition (internal) error.  
***SWD\_ERROR\_NULLCONTEXT*** Null context pointer.  
***SWD\_ERROR\_QUEUE*** Queue error.  
***SWD\_ERROR\_ADDR*** Addressing error.  
***SWD\_ERROR\_APnDP*** Bad APnDP value.  
***SWD\_ERROR\_RnW*** Bad RnW value.  
***SWD\_ERROR\_PARITY*** Parity error.  
***SWD\_ERROR\_ACK*** Acknowledge error.  
***SWD\_ERROR\_ACKUNKNOWN*** Unknown ACK value.  
***SWD\_ERROR\_ACKNOTDONE*** ACK not yet executed on target.  
***SWD\_ERROR\_ACKMISSING*** ACK command not found on the queue.  
***SWD\_ERROR\_ACKMISMATCH*** Bad ACK result address.  
***SWD\_ERROR\_ACKORDER*** ACK not in order REQ->TRN->ACK.  
***SWD\_ERROR\_BADOPCODE*** Unsupported operation requested.  
***SWD\_ERROR\_NODATACMD*** Command not found on the queue.  
***SWD\_ERROR\_DATAADDR*** Bad DATA result address.  
***SWD\_ERROR\_NOPARITYCMD*** Parity command missing or misplaced.  
***SWD\_ERROR\_PARITYADDR*** Bad PARITY command result address.  
***SWD\_ERROR\_NOTDONE*** Could not end selected task.  
***SWD\_ERROR\_QUEUE\_ROOT*** Queue root not found or null.  
***SWD\_ERROR\_BADCMDTYPE*** Unknown command detected.  
***SWD\_ERROR\_BADCMDDATA*** Bad command data.

***SWD\_ERROR\_TURNAROUND*** Error during turnaround switch.  
***SWD\_ERROR\_DRIVER*** Driver error.  
***SWD\_ERROR\_ACK\_WAIT*** Received ACK WAIT.  
***SWD\_ERROR\_ACKFAULT*** Received ACK FAULT.  
***SWD\_ERROR\_QUEUENOTFREE*** Cannot free resources, queue not empty.  
***SWD\_ERROR\_TRANSPORT*** Transport type unknown or undefined.

#### 5.2.4.4 enum SWD\_LOGLEVEL

Logging Level Codes definition.

Logging Level codes definition, use this to have its name on debugger.

**Enumerator:**

***SWD\_LOGLEVEL\_SILENT*** Remain silent.  
***SWD\_LOGLEVEL\_INFO*** Log only informational messages.  
***SWD\_LOGLEVEL\_WARNING*** also log warnings.  
***SWD\_LOGLEVEL\_ERROR*** also log errors.  
***SWD\_LOGLEVEL\_DEBUG*** Log everything including detailed details.

#### 5.2.4.5 enum SWD\_OPERATION

Command Queue operations codes.

**Enumerator:**

***SWD\_OPERATION\_FIRST*** First operation to know its code.  
***SWD\_OPERATION\_QUEUE\_APPEND*** Append command(s) to the queue.  
***SWD\_OPERATION\_TRANSMIT\_HEAD*** Transmit root..current (head).  
***SWD\_OPERATION\_TRANSMIT\_TAIL*** Transmit current..last (tail).  
***SWD\_OPERATION\_TRANSMIT\_ALL*** Transmit all commands on the queue.  
***SWD\_OPERATION\_TRANSMIT\_ONE*** Transmit only current command.  
***SWD\_OPERATION\_TRANSMIT\_LAST*** Transmit last command on the queue.  
***SWD\_OPERATION\_EXECUTE*** Queue commands then flush the queue.  
***SWD\_OPERATION\_LAST*** Last operation to know its code.

#### 5.2.4.6 enum SWD\_SHIFTDIR

What is the shift direction LSB-first or MSB-first.

**Enumerator:**

***SWD\_DIR\_LSBFIRST*** Data is shifted in/out right (LSB-first).  
***SWD\_DIR\_MSBFIRST*** Data is shifted in/out left (MSB-first).

## 5.2.5 Function Documentation

### 5.2.5.1 int swd\_bin32\_bitswap ( unsigned int \* *buffer*, int *bitcount* )

Bit swap helper function that reverse bit order in int \*buffer.

Most Significant Bit becomes Least Significant Bit. It is possible to swap only n-bits from int (32-bit) \*buffer.

#### Parameters

\**buffer* unsigned char (32-bit) data pointer.

*bitcount* how many bits to swap.

#### Returns

swapped bit count (positive) or error code (negative).

### 5.2.5.2 int swd\_bin32\_parity\_even ( int \* *data*, char \* *parity* )

Data parity calculator, calculates even parity on integer type.

#### Parameters

\**data* source data pointer.

\**parity* resulting data pointer.

#### Returns

negative value on error, 0 or 1 as parity result.

### 5.2.5.3 int swd\_bin32\_print ( int \* *data* )

Prints binary data of an integer value on the screen.

#### Parameters

\**data* source data pointer.

#### Returns

number of characters printed.

### 5.2.5.4 char\* swd\_bin32\_string ( int \* *data* )

Generates string containing binary data of an integer value.

#### Parameters

\**data* source data pointer.

#### Returns

pointer to the resulting string.

**5.2.5.5 int swd\_bin8\_bitswap ( unsigned char \* *buffer*, int *bitcount* )**

Bit swap helper function that reverse bit order in char \*buffer.

Most Significant Bit becomes Least Significant Bit. It is possible to swap only n-bits from char (8-bit) \*buffer.

**Parameters**

\**buffer* unsigned char (8-bit) data pointer.

*bitcount* how many bits to swap.

**Returns**

swapped bit count (positive) or error code (negative).

**5.2.5.6 int swd\_bin8\_parity\_even ( char \* *data*, char \* *parity* )**

Data parity calculator, calculates even parity on char type.

**Parameters**

\**data* source data pointer.

\**parity* resulting data pointer.

**Returns**

negative value on error, 0 or 1 as parity result.

**5.2.5.7 int swd\_bin8\_print ( char \* *data* )**

Prints binary data of a char value on the screen.

**Parameters**

\**data* source data pointer.

**Returns**

number of characters printed.

**5.2.5.8 char\* swd\_bin8\_string ( char \* *data* )**

Generates string containing binary data of a char value.

**Parameters**

\**data* source data pointer.

**Returns**

pointer to the resulting string.

**5.2.5.9 int swd\_bit8\_gen\_request ( *swd\_ctx\_t* \* *swdctx*, *char* \* *APnDP*, *char* \* *RnW*, *char* \* *addr*, *char* \* *request* )**

Generate 8-bit SWD-REQUEST packet contents with provided parameters.

Note that parity bit value is calculated automatically.

#### Parameters

\**swdctx* swd context pointer.  
\**APnDP* AccessPort (high) or DebugPort (low) access type pointer.  
\**RnW* Read (high) or Write (low) operation type pointer.  
\**addr* target register address value pointer.  
\**request* pointer where to store resulting packet.

#### Returns

number of generated packets (1), or SWD\_ERROR\_CODE on failure.

**5.2.5.10 int swd\_bus\_setdir\_miso ( *swd\_ctx\_t* \* *swdctx* )**

Append command queue with TRN READ/MISO.

#### Parameters

\**swdctx* swd context pointer.

#### Returns

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.2.5.11 int swd\_bus\_setdir\_mosi ( *swd\_ctx\_t* \* *swdctx* )**

Append command queue with TRN WRITE/MOSI.

#### Parameters

\**swdctx* swd context pointer.

#### Returns

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.2.5.12 int swd\_cmd\_append\_mosi\_n\_data\_ap ( *swd\_ctx\_t* \* *swdctx*, *int* \*\* *data*, *int* *count* )**

Append command queue with series of data and automatic parity writes.

#### Parameters

\**swdctx* swd context pointer.  
\*\**data* data value array pointer.

**count** number of (data+parity) elements to read.

#### Returns

number of elements appended (2\*count), or SWD\_ERROR\_CODE on failure.

**5.2.5.13 int swd\_cmd\_append\_mosi\_n\_data\_p ( swd\_ctx\_t \* swdctx, int \*\* data, char \*\* parity, int count )**

Append command queue with series of data and provided parity writes.

#### Parameters

\***swdctx** swd context pointer.

\*\***data** data value array pointer.

\*\***parity** parity value array pointer.

**count** number of (data+parity) elements to read.

#### Returns

number of elements appended (2\*count), or SWD\_ERROR\_CODE on failure.

**5.2.5.14 int swd\_cmd\_queue\_append ( swd\_cmd\_t \* cmdq, swd\_cmd\_t \* cmd )**

Append element pointed by \*cmd at the end of the quque pointed by \*cmdq.

#### Parameters

\***cmdq** pointer to any element on command queue

\***cmd** pointer to the command to be appended

#### Returns

number of appended elements (one), SWD\_ERROR\_CODE on failure

**5.2.5.15 int swd\_cmd\_queue\_append\_jtag2swd ( swd\_ctx\_t \* swdctx )**

Append command queue with JTAG-TO-SWD DAP-switch sequence.

#### Parameters

\***swdctx** swd context pointer.

#### Returns

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.2.5.16 int swd\_cmd\_queue\_append\_miso\_ack ( *swd\_ctx\_t* \* *swdctx*, *char* \* *ack* )**

Append queue with ACK read.

**Parameters**

\**swdctx* swd context pointer.  
\**ack* packet value pointer.

**Returns**

number of elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.2.5.17 int swd\_cmd\_queue\_append\_miso\_data ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data* )**

Append command queue with data read.

**Parameters**

\**swdctx* swd context pointer.  
\**data* data pointer.

**Returns**

of elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.2.5.18 int swd\_cmd\_queue\_append\_miso\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data*, *char* \* *parity* )**

Append command queue with data and parity read.

**Parameters**

\**swdctx* swd context pointer.  
\**data* data value pointer.  
\**parity* parity value pointer.

**Returns**

number of elements appended (2), or SWD\_ERROR\_CODE on failure.

**5.2.5.19 int swd\_cmd\_queue\_append\_miso\_n\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *int* \*\* *data*, *char* \*\* *parity*, *int* *count* )**

Append command queue with series of data and parity read.

**Parameters**

\**swdctx* swd context pointer.  
\*\**data* data value array pointer.

***\*\*parity*** parity value array pointer.  
***count*** number of (data+parity) elements to read.

### Returns

number of elements appended (2\*count), or SWD\_ERROR\_CODE on failure.

## 5.2.5.20 int swd\_cmd\_queue\_append\_miso\_nbit ( *swd\_ctx\_t \* swdctx, char \*\* data, int count* )

Append command queue with bus binary read bit-by-bit operation.

This function will append command to the queue for each bit, and store one bit into single char array element, so read is not constrained to 8 bits. On error memory is released and appropriate error code is returned. Important: Memory pointed by \*data must be allocated prior call!

### Parameters

***\*swdctx*** swd context pointer.  
***\*\*data*** allocated data array to write result into.  
***count*** number of bits to read (also the \*\*data size).

### Returns

number of elements processed, or SWD\_ERROR\_CODE on failure.

## 5.2.5.21 int swd\_cmd\_queue\_append\_miso\_parity ( *swd\_ctx\_t \* swdctx, char \* parity* )

Append command queue with parity bit read.

### Parameters

***\*swdctx*** swd context pointer.  
***\*parity*** parity value pointer.

### Returns

number of elements appended (1), or SWD\_ERROR\_CODE on failure.

## 5.2.5.22 int swd\_cmd\_queue\_append\_miso\_trn ( *swd\_ctx\_t \* swdctx* )

Append command queue with Turnaround activating MISO mode.

### Parameters

***\*swdctx*** swd context pointer.

### Returns

return number of elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.2.5.23 int swd\_cmd\_queue\_append\_mosi\_control ( *swd\_ctx\_t* \* *swdctx*, *char* \* *ctlmsg*, *int* *len* )**

Append command queue with len-octet size control seruence.

This control sequence can be used for instance to send payload of packets switching DAP between JTAG and SWD mode.

#### Parameters

\**swdctx* swd context pointer.  
\**ctlmsg* control message array pointer.  
*len* number of elements to send from \**ctlmsg*.

#### Returns

number of elements appended (*len*), or SWD\_ERROR\_CODE on failure.

**5.2.5.24 int swd\_cmd\_queue\_append\_mosi\_data ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data* )**

Append command queue with data and parity write.

#### Parameters

\**swdctx* swd context pointer.  
\**data* data value pointer.

#### Returns

number of elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.2.5.25 int swd\_cmd\_queue\_append\_mosi\_data\_ap ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data* )**

Append command queue with data and automatic parity write.

#### Parameters

\**swdctx* swd context pointer.  
\**data* data value pointer.

#### Returns

number of elements appended (2), or SWD\_ERROR\_CODE on failure.

**5.2.5.26 int swd\_cmd\_queue\_append\_mosi\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *int* \* *data*, *char* \* *parity* )**

Append command queue with data and provided parity write.

#### Parameters

\**swdctx* swd context pointer.

\****data*** data value pointer.  
\****parity*** parity value pointer.

#### Returns

number of elements appended (2), or SWD\_ERROR\_CODE on failure.

### 5.2.5.27 int swd\_cmd\_queue\_append\_mosi\_nbit ( ***swd\_ctx\_t*** \* ***swdctx***, ***char*** \* ***data***, ***int*** ***count*** )

Append command queue with bus binary write bit-by-bit operation.

This function will append command to the queue for each bit and store one bit into single char array element, so read is not constrained to 8 bits. On error memory is released and appropriate error code is returned. Important: Memory pointed by \*data must be allocated prior call!

#### Parameters

\****swdctx*** swd context pointer.  
\*\****data*** allocated data array to write result into.  
***count*** number of bits to read (also the \*\*data size).

#### Returns

number of elements processed, or SWD\_ERROR\_CODE on failure.

### 5.2.5.28 int swd\_cmd\_queue\_append\_mosi\_parity ( ***swd\_ctx\_t*** \* ***swdctx***, ***char*** \* ***parity*** )

Append command queue with parity bit write.

#### Parameters

\****swdctx*** swd context pointer.  
\****parity*** parity value pointer.

#### Returns

number of elements appended (1), or SWD\_ERROR\_CODE on failure.

### 5.2.5.29 int swd\_cmd\_queue\_append\_mosi\_request ( ***swd\_ctx\_t*** \* ***swdctx***, ***char*** \* ***request*** )

Appends command queue with SWD Request packet header.

Note that contents is not validated, so bad request can be sent as well.

#### Parameters

\****swdctx*** swd context pointer.  
\****request*** pointer to the 8-bit request payload.

#### Returns

return number elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.2.5.30 int swd\_cmd\_queue\_append\_mosi\_trn ( swd\_ctx\_t \* swdctx )**

Append command queue with Turnaround activating MOSI mode.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

return number elements appended (1), or SWD\_ERROR\_CODE on failure.

**5.2.5.31 int swd\_cmd\_queue\_append\_swd2jtag ( swd\_ctx\_t \* swdctx )**

Append command queue with SWD-TO-JTAG DAP-switch sequence.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.2.5.32 int swd\_cmd\_queue\_append\_swdpreset ( swd\_ctx\_t \* swdctx )**

Append command queue with SW-DP-RESET sequence.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

number of elements appended, or SWD\_ERROR\_CODE on failure.

**5.2.5.33 swd\_cmd\_t\* swd\_cmd\_queue\_find\_root ( swd\_cmd\_t \* cmdq )**

Find queue root (first element).

**Parameters**

\**cmdq* pointer to any queue element

**Returns**

swd\_cmd\_t\* pointer to the first element (root), NULL on failure

**5.2.5.34 swd\_cmd\_t\* swd\_cmd\_queue\_find\_tail ( swd\_cmd\_t \* cmdq )**

Find queue tail (last element).

**Parameters**

*\*cmdq* pointer to any queue element

**Returns**

swd\_cmd\_t\* pointer to the last element (tail), NULL on failure

**5.2.5.35 int swd\_cmd\_queue\_flush ( swd\_ctx\_t \* swdctx, swd\_operation\_t operation )**

Flush command queue contents into interface driver.

Operation is specified by SWD\_OPERATION and can be used to select how to flush the queue, ie. head-only, tail-only, one, all, etc.

**Parameters**

*\*swdctx* swd context pointer.

*operation* tells how to flush the queue.

**Returns**

number of commands transmitted, or SWD\_ERROR\_CODE on failure.

**5.2.5.36 int swd\_cmd\_queue\_free ( swd\_cmd\_t \* cmdq )**

Free queue pointed by \*cmdq element.

**Parameters**

*\*cmdq* pointer to any element on command queue

**Returns**

number of elements destroyed, SWD\_ERROR\_CODE on failure

**5.2.5.37 int swd\_cmd\_queue\_free\_head ( swd\_cmd\_t \* cmdq )**

Free queue head up to \*cmdq element.

**Parameters**

*\*cmdq* pointer to the element that becomes new queue root.

**Returns**

number of elements destroyed, or SWD\_ERROR\_CODE on failure.

**5.2.5.38 int swd\_cmd\_queue\_free\_tail ( *swd\_cmd\_t* \* *cmdq* )**

Free queue tail starting after \*cmdq element.

**Parameters**

\**cmdq* pointer to the last element on the new queue.

**Returns**

number of elements destroyed, or SWD\_ERROR\_CODE on failure.

**5.2.5.39 int swd\_cmd\_queue\_init ( *swd\_cmd\_t* \* *cmdq* )**

Initialize new queue element in memory that becomes a queue root.

**Parameters**

\**cmdq* pointer to the command queue element of type [swd\\_cmd\\_t](#)

**Returns**

SWD\_OK on success, SWD\_ERROR\_CODE code on failure

**5.2.5.40 int swd\_deinit ( *swd\_ctx\_t* \* *swdctx* )**

De-initialize selected swd context and its command queue.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

number of elements freed, or SWD\_ERROR\_CODE on failure.

**5.2.5.41 int swd\_deinit\_cmdq ( *swd\_ctx\_t* \* *swdctx* )**

De-initialize command queue and free its memory on selected swd context.

**Parameters**

\**swdctx* swd context pointer.

**Returns**

number of commands freed, or SWD\_ERROR\_CODE on failure.

### 5.2.5.42 int swd\_deinit\_ctx ( *swd\_ctx\_t* \* *swdctx* )

De-initialize selected swd context and free its memory.

Note: This function will not free command queue for selected context!

#### Parameters

*\*swdctx* swd context pointer.

#### Returns

SWD\_OK on success, SWD\_ERROR\_CODE on failure.

### 5.2.5.43 int swd\_idcode ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *int* \* *idcode*, *char* \* *ack*, *char* \* *parity* )

Read target's IDCODE register value.

#### Parameters

*\*swdctx* swd context pointer.

*operation* type of action to perform (queue or execute).

*\*idcode* resulting register value pointer.

*\*ack* resulting acknowledge response value pointer.

*\*parity* resulting data parity value pointer.

#### Returns

number of elements processed on the queue, or SWD\_ERROR\_CODE on failure.

### 5.2.5.44 *swd\_ctx\_t*\* swd\_init ( void )

LibSWD initialization routine.

It should be called prior any operation made with libswd. It initializes command queue and basic parameters for context that is returned as pointer.

#### Returns

pointer to the initialized swd context.

### 5.2.5.45 int swd\_jtag2swd ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation* )

Activate SW-DP and deactivate JTAG-DP by sending out JTAG-TO-SWD sequence.

#### Parameters

*\*swdctx* swd context.

#### Returns

number of control bytes executed, or error code on failre.

**5.2.5.46 int swd\_miso\_ack ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *char* \* *ack* )**

Perform ACK read into \*ack and verify received data.

**Parameters**

\**swdctx* swd context pointer.

*operation* type of action to perform with generated request.

\**ack* pointer to the result location.

**Returns**

number of commands processed, or SWD\_ERROR\_CODE on failure.

**5.2.5.47 int swd\_miso\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *int* \* *data*, *char* \* *parity* )**

Perform (MISO) data read.

**Parameters**

\**swdctx* swd context pointer.

*operation* type of action to perform on generated command.

\**data* payload value pointer.

\**parity* payload parity value pointer.

**Returns**

number of elements processed, or SWD\_ERROR\_CODE on failure.

**5.2.5.48 int swd\_mosi\_data\_ap ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *int* \* *data* )**

Perform (MOSI) data write with automatic parity calculation.

**Parameters**

\**swdctx* swd context pointer.

*operation* type of action to perform on generated command.

\**data* payload value pointer.

**Returns**

number of elements processed, or SWD\_ERROR\_CODE on failure.

**5.2.5.49 int swd\_mosi\_data\_p ( *swd\_ctx\_t* \* *swdctx*, *swd\_operation\_t* *operation*, *int* \* *data*, *char* \* *parity* )**

Perform (MOSI) data write with provided parity value.

**Parameters**

*\*swdctx* swd context pointer.  
*operation* type of action to perform on generated command.  
*\*data* payload value pointer.  
*\*parity* payload parity value pointer.

**Returns**

number of elements processed, or SWD\_ERROR\_CODE on failure.

**5.2.5.50 int swd\_mosi\_jtag2swd ( swd\_ctx\_t \* swdctx, swd\_operation\_t operation )**

Switch DAP into SW-DP.

According to ARM documentation target's DAP use JTAG transport by default and so JTAG-DP is active after power up. To use SWD user must perform predefined sequence on SWDIO/TMS lines, then read out the IDCODE to ensure proper SW-DP operation.

**5.2.5.51 int swd\_mosi\_request ( swd\_ctx\_t \* swdctx, swd\_operation\_t operation, char \* APnDP, char \* RnW, char \* addr )**

Perform Request.

**Parameters**

*\*swdctx* swd context pointer.  
*operation* type of action to perform with generated request.  
*\*APnDP* AccessPort (high) or DebugPort (low) access value pointer.  
*\*RnW* Read (high) or Write (low) access value pointer.  
*\*addr* target register address value pointer.

**Returns**

number of commands processed, or SWD\_ERROR\_CODE on failure.

**5.2.5.52 int swd\_transmit ( swd\_ctx\_t \* swdctx, swd\_cmd\_t \* cmd )**

Transmit selected command to the interface driver.

**Parameters**

*\*swdctx* swd context pointer.  
*\*cmd* pointer to the command to be sent.

**Returns**

number of commands transmitted (1), or SWD\_ERROR\_CODE on failure.